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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

501164

APPL NUM 10065717	FILING DATE 11/13/2002	CLASS 438	SUBCLASS 500	GAU 4765	EXAMINER
**APPLICANTS: Yang Ming-Sheng, Kao Chia, Chien Chin Cheng.					
**CONTINUING DATA VERIFIED: This application is a CIP of 09/683,691 02/04/2002					
** FOREIGN APPLICATIONS VERIFIED:					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO. NAUP0504USA	
Verified and Acknowledged Examiner's Initials					
TITLE : Method of preventing leakage current of a metal oxide semiconductor transistor					

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Figs.Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner	
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